

# Abstracts

## A Novel Phase Noise Reduction Technique for MMIC Frequency Synthesizers that Uses Newly Developed Pulse Generator LSI

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*T. Nakagawa and T. Ohira. "A Novel Phase Noise Reduction Technique for MMIC Frequency Synthesizers that Uses Newly Developed Pulse Generator LSI." 1994 MTT-S International Microwave Symposium Digest 94.1 (1994 Vol. 1 [MWSYM]): 95-98.*

A novel phase noise reduction technique is proposed for PLL frequency synthesizers. The key circuits are fabricated in one LSI, and successfully applied to a C-band MMIC frequency synthesizer. The resultant phase noise reduction is a remarkable 10 dB.

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